

APPLICATION DATA SHEET

Electronic Version v14

Stylesheet Version v14.0

Title of Invention	A SYSTEM AND METHOD OF PROVIDING ERROR DETECTION AND CORRECTION CAPABILITY IN AN INTEGRATED CIRCUIT USING REDUNDANT LOGIC CELLS OF AN EMBEDDED FPGA
Application Type :	regular, utility
Attorney Docket Number :	BUR920040001US1
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Customer Number:	23389
	
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as our attorney(s) or agent(s) to prosecute the application identified above, and to transact all business in the United States Patent and Trademark Office connected therewith.

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